

3.1.1.3 PRC (DZZSP58029)

(1) Block Explanation

The DZZSP58029 is an LSI which achieves refresh control of the general-use programmable port and Pseudo RAM as well as control of peripheral devices.

- (a) Pseudo RAM Refresh
Refresh Signal
RAM Chip Select x2
- (b) Programmable I/O Port
I/O Port 8 bit x 3, 2 bit x 1
Output Port 8 bit x 3
- (c) Panel Serial Interface
Data length : 16 bits
Transfer clock 250KHz
- (d) Thermal Head Clock Control
- (e) Watch Dog Timer
- (f) Address Decoder

(2) Block Diagram

