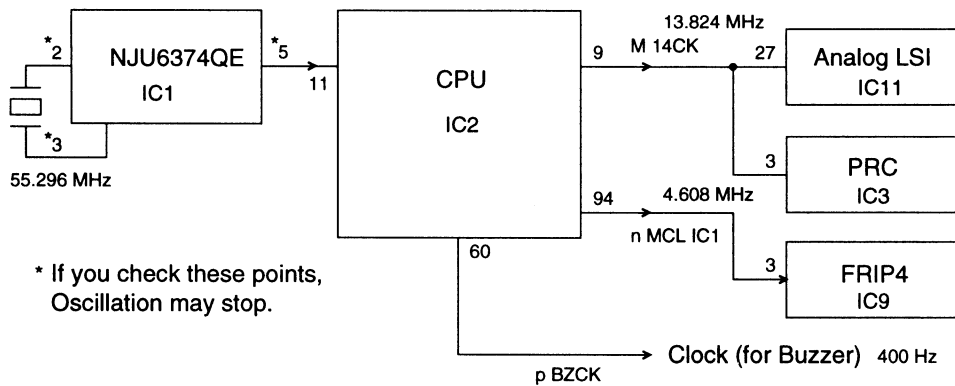
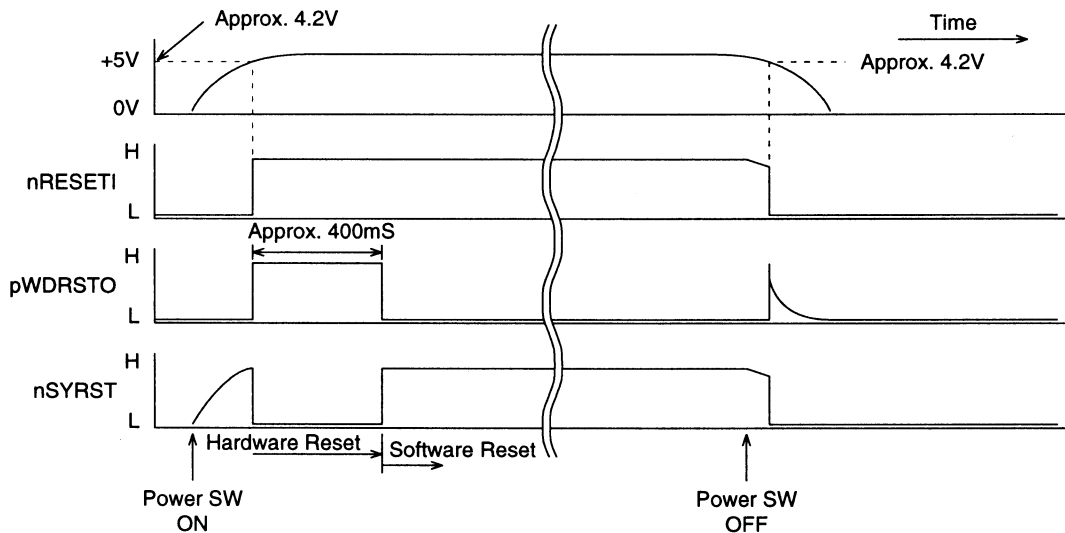
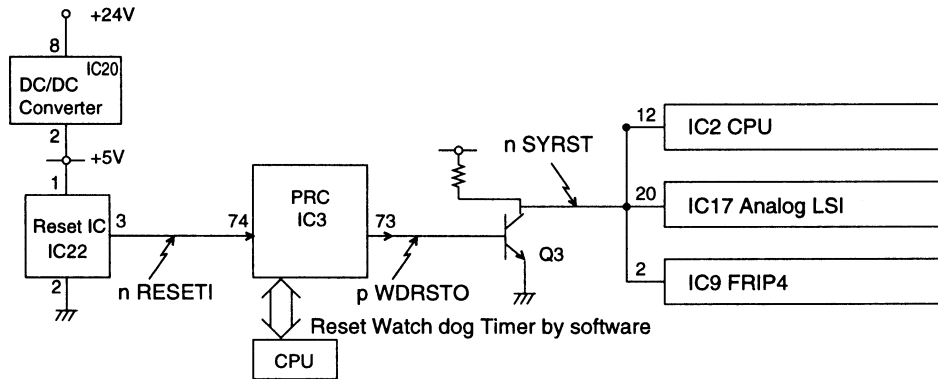


2.1.3 Reset Circuit

This +5 V system voltage is monitored by IC22 (the reset IC), and nRESETI becomes high level when the voltage of pin1 exceeds +4.2 V. IC3 (PRC) which contains a Watchdog Reset Circuit starts to check the peripheral circuits by high level of nRESETI signal and about 400 ms later the Watchdog Reset (pWDRSTO) is output with a change from high to low level.

Plugging the Power Cord or the +5 V system voltage drops and reaches the +4.2 V point, the hardware reset (nHDRES) operates and the functions of the microprocessors are stopped.

2.1.4 System Clock Circuit



* If you check these points, Oscillation may stop.